

European Microcontroller Design Centre
Fujitsu Microelectronics Europe GmbH
Pittlerstrasse 47
63225 Langen, Germany



TEMPUS JEP_41107 2006
To Prof. V. Litovski

July 20, 2008, Langen

Subject: The SoC curriculum at the faculty of Electronic Engineering in Niš.

Dear Sir,

Thank you for the letter of May 30, 2008. Here is my observation related to the subject.

1. General

I am very glad that faculty of Electronic Engineering in Niš shows a clear tendency to become fully compatible with the European University educational system. The way how the topic of electronic design automation is introduced and organized is outstanding. Although the teaching material is very broad based and challenging, it is introduced gradually by preparing a good basis in electronics on bachelor Studies in order to target very complicated and wide area of SoC later on master level Studies. Since SoC is becoming more and more important in modern electronic engineering I strongly support your proposal. It is nice to see that the decade long tradition in this field is continued and has well planned future in Niš.

2. Specific

The structure of the curriculum is well planned and organized. I am especially pleased that modern software tools are introduced, making possible to cope with ambitious and demanding projects and get a practical experience regarding all sort of problems in an IC design flow. Moreover, the basis behind automation tools (modeling of components on various abstraction levels, simulation and optimization algorithms etc.) is not neglected, providing wide and solid knowledge base, that is getting more and more important over engineering work life, due to very dynamic changes in the electronic industry.

I suggest to insist more on practical exercises and 'try and error' way of learning.

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3. Here are my comments related to the syllabi.
 - a. All relevant topics and key aspects are covered.
 - b. The course content is modern and up-to-date.

I suggest to pay more attention on application areas and trends of electronic systems. Nowadays, they decide essentially the architecture of SoC and define main design problems and obstacles. Also, it would be nice to introduce the most powerful and distributed system architectures (Cortex, ARM, etc.).

With best regards,

A handwritten signature in purple ink, which appears to read "Saša Janković".

Saša Janković