Prof. Vančo Litovski
Elektronski Fakultet
Niš
Serbia

Niš, June 24, 2009.

Subject: The round table on Joint Tempus Projects Workshop "SoCD & JADES"
     held within the conference ETRAN 2009, in Vrnjačka Banja.
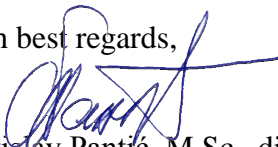
Dear Prof. Litovski.

First of all I want to thank you for the invitation to participate at the Round Table as mentioned above. It is my feeling that the whole meeting including presentations of the project participants and the discussion of the audience was extremely successful. Events of this kind, in my opinion, are indispensable both for promoting the results of the projects and for influence the educational community to reconsider their own curriculum. It is not to forget that we had the opportunity to express our opinion regarding the very curriculum and the way how it is planned to be implemented.

System on Chip Design is a contemporary and, we may say, advanced concept that has to be thought at the Serbian Universities. Since before the Round Table I had the opportunity to visit the web site of the project I was able to follow all discussions. That I suppose was the case with most of the participants.

It is my conclusion that both, participants from Universities, and from the business community were generally satisfied with the content of the curriculum and expressed good feelings regarding its sustainability.

From the Regional Chamber of Commerce (that I am representing) point of view this curriculum and similar effort are welcomed having in mind they are giving a diversified knowledge base to the Serbian electronic engineering community. We really hope that this effort will help in rising the aspirations of Serbian and foreign firms involved in the subject while, in the same time, helping in lowering the number of unemployed engineers.

With best regards,

Svetislav Pantić, M.Sc., dipl. Eng. In electronics
Regional Chamber fo Commerce, Niš